

FIG. 1

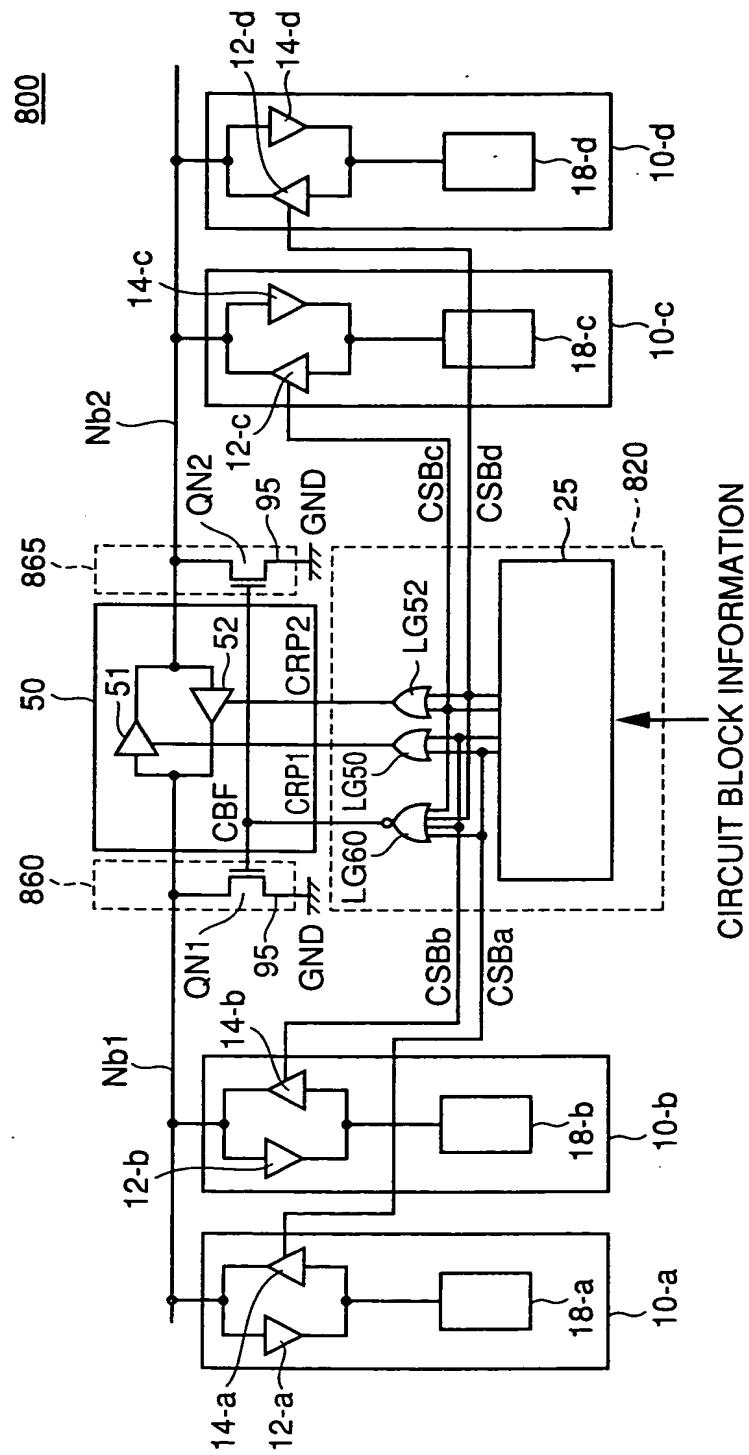


FIG. 2

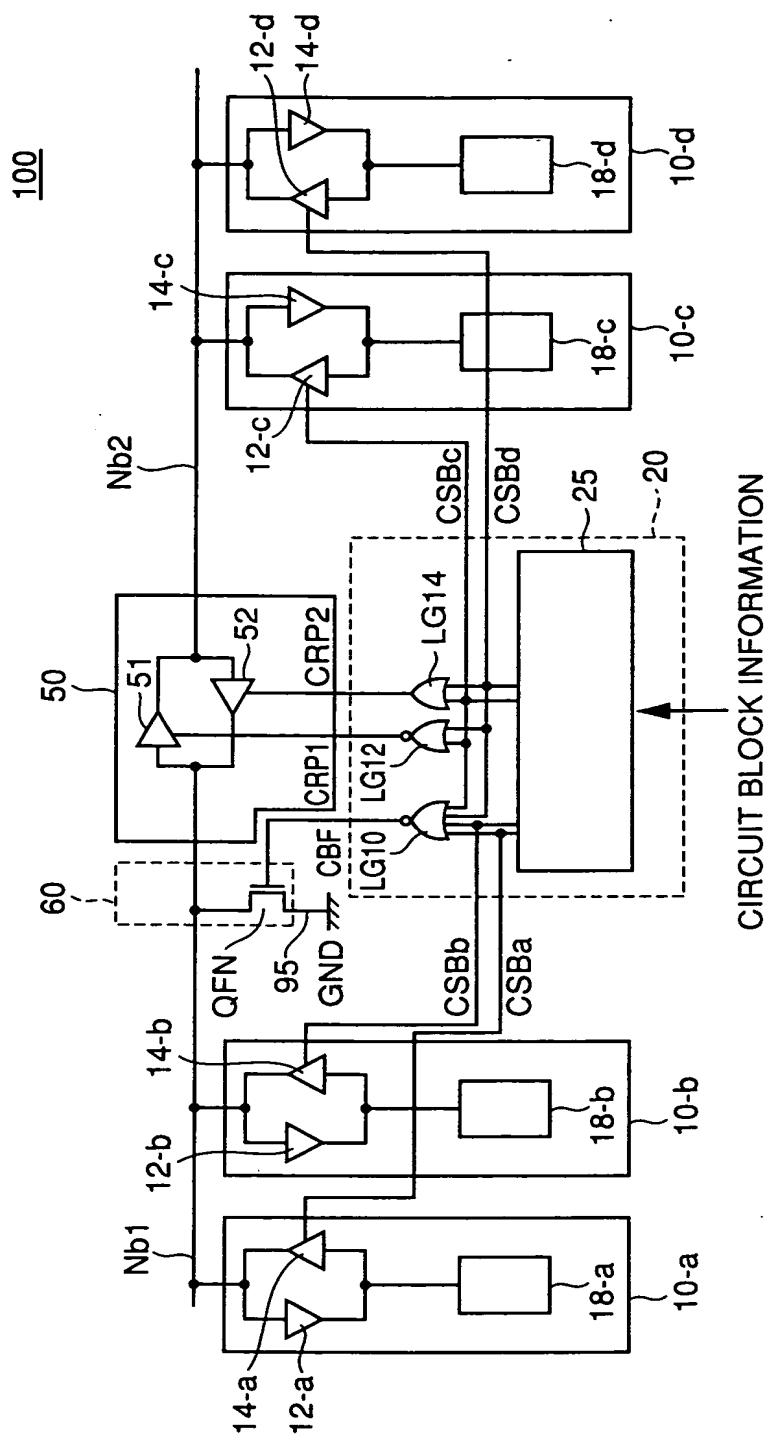


FIG. 3

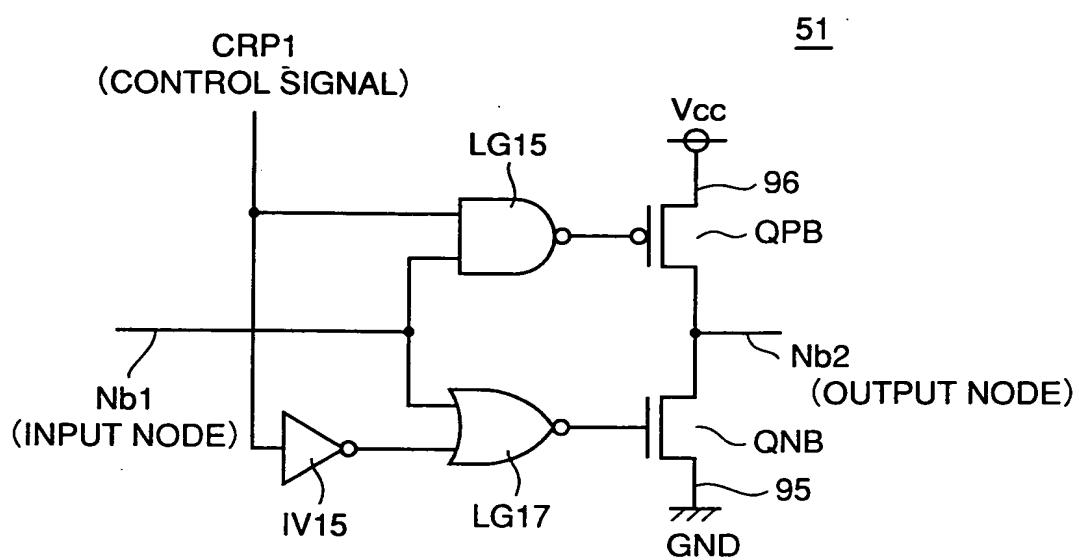


FIG. 4

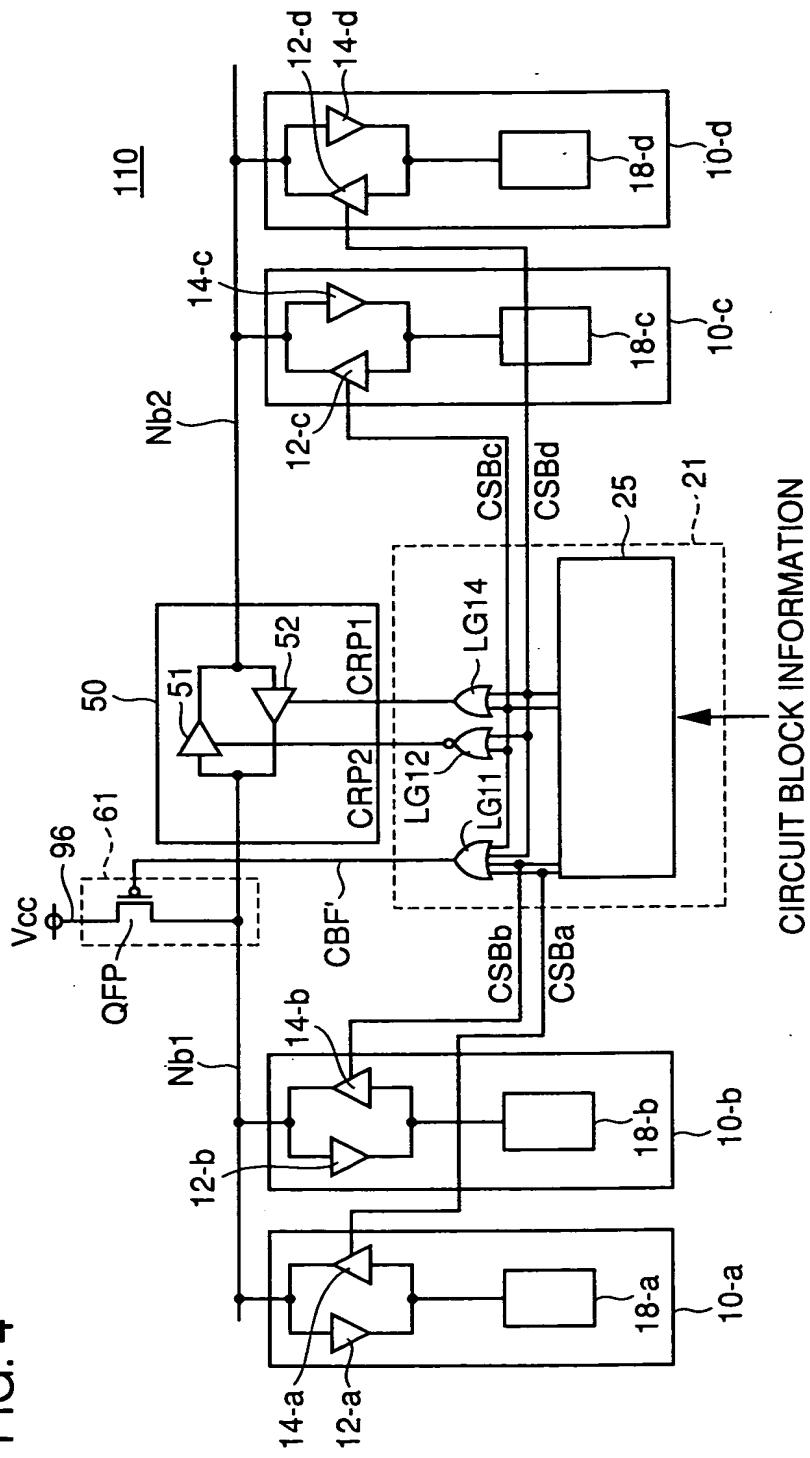
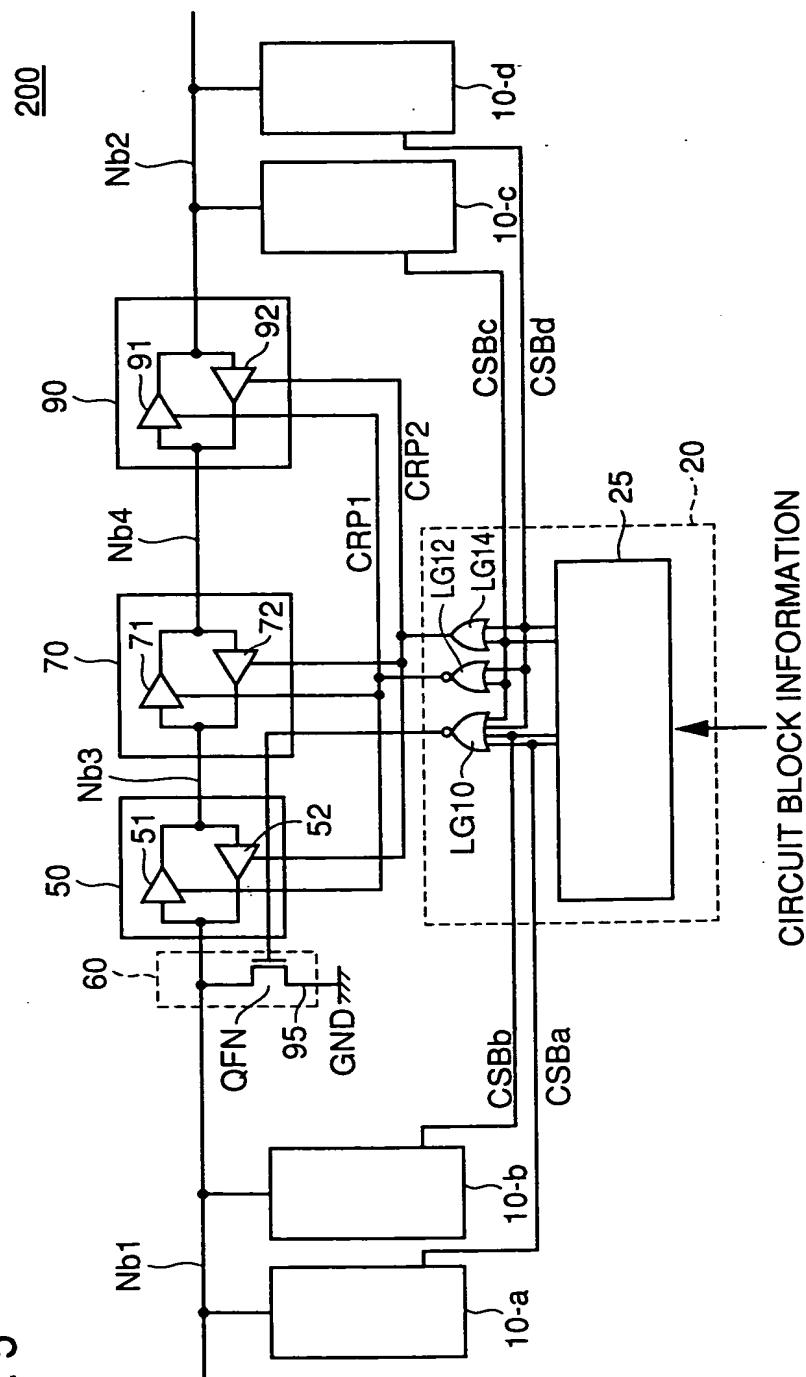


FIG. 5



CIRCUIT BLOCK INFORMATION

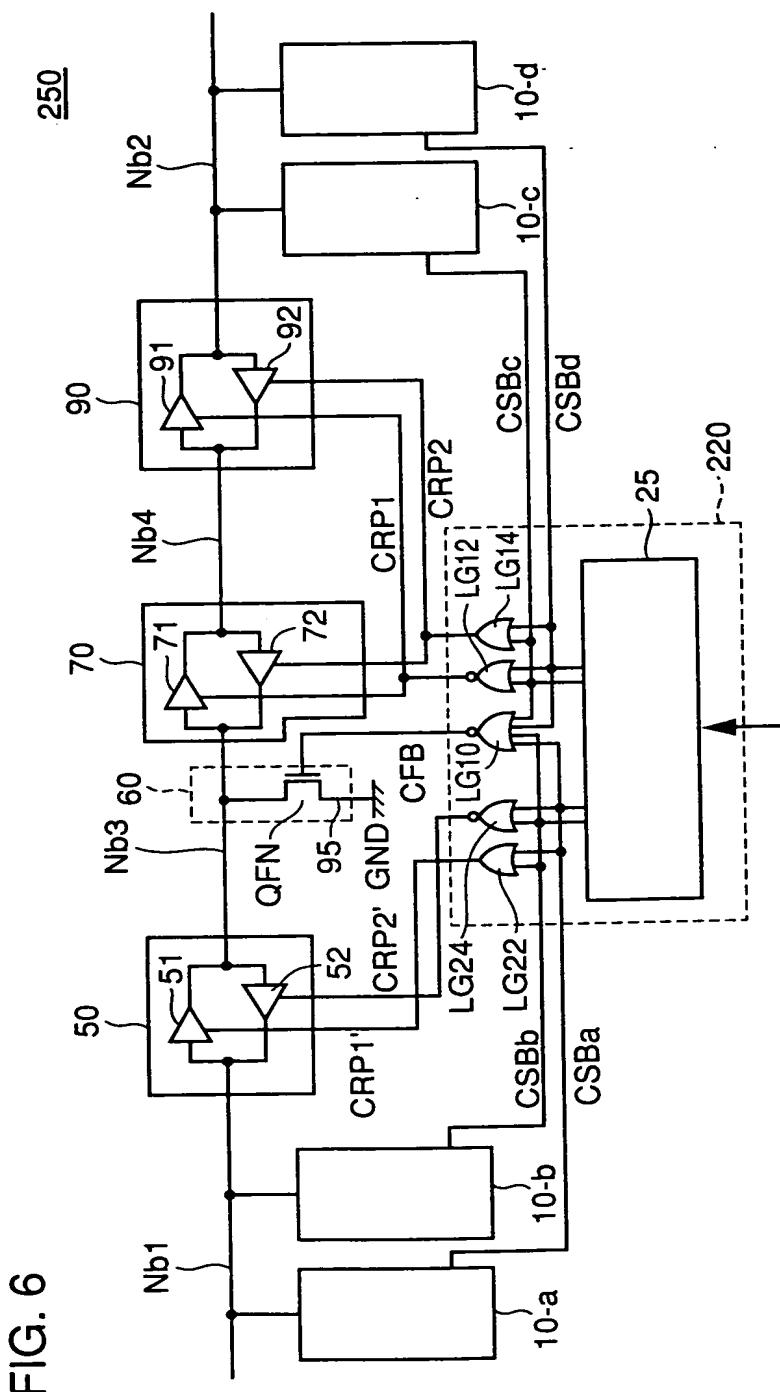


FIG. 6

FIG. 7

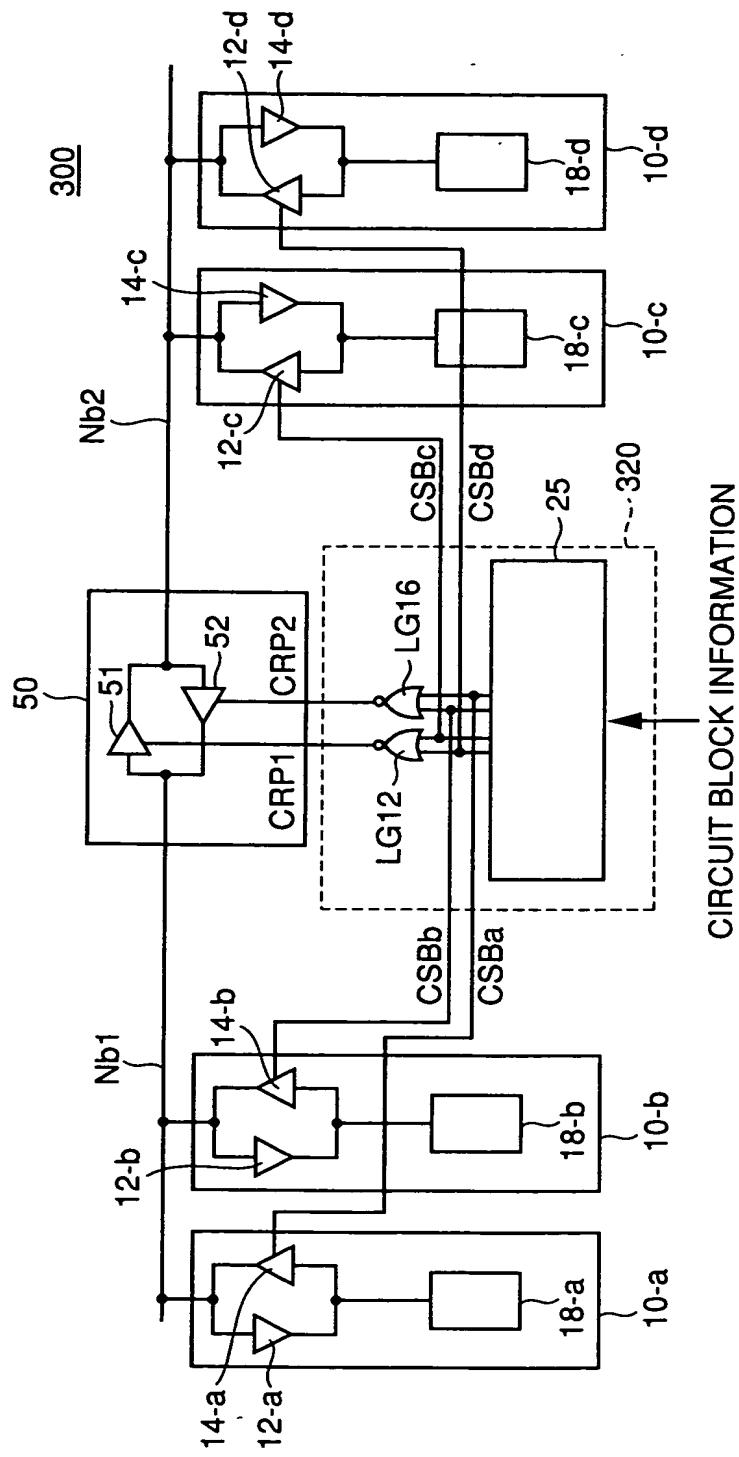


FIG. 8

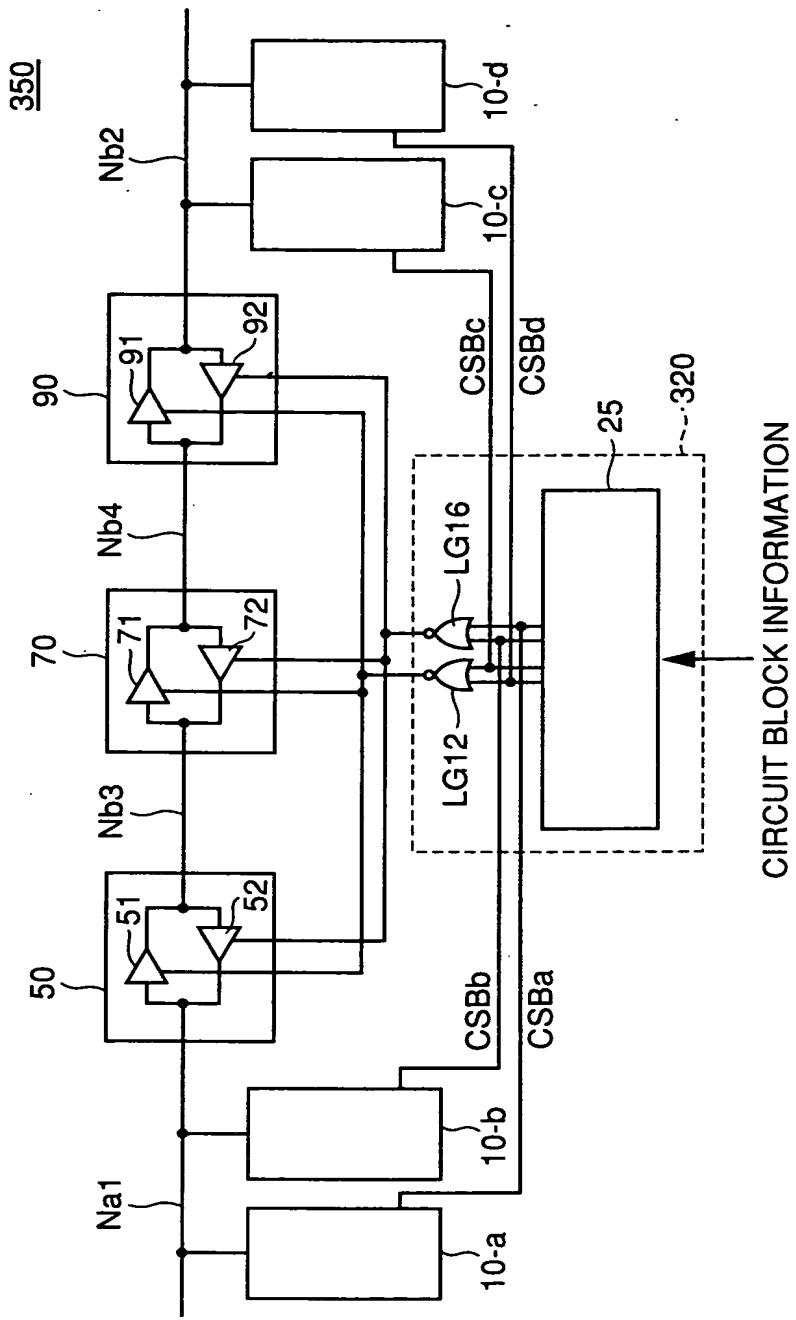


FIG. 9 PRIOR ART

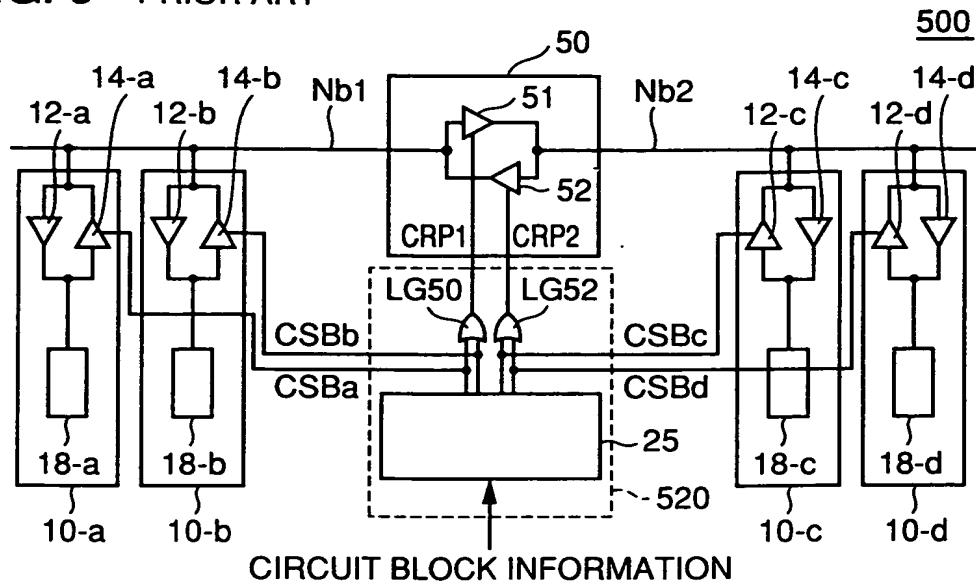


FIG. 10 PRIOR ART

